

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			Docket Number (Optional) FIS920020038	Application Number 10/614,961				
			Applicant(s) Mandelman et al.					
			Filing Date 7/08/2003	Group Art Unit 2812				
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
JK		6,238,967	05-29-01	Shiho et al.				
JK		6,066,527	05-23-00	Kudelka et al.				
JK		6,232,170	05-15-01	Hakey et al.				
JK		6,184,107	02-06-01	Divakaruni et al.				
JK		6,063,657	05-16-00	Bronner et al.				
JK		6,204,112	03-20-01	Chakravarti et al.				
FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
JK		WO 00/13225	03-09-00	PCT				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
JK		"Silicon VLSI Technology Fundamentals, Practice and Modeling" Plummer et al., Department of Electrical Engineering, Stanford University, Prentice Hall Pub., ppgs. 644-647						
EXAMINER Jennifer M. Kennedy			DATE CONSIDERED April 14, 2004					
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								